

Search Results -

| Term | Documents |
|---|-----------|
| CONTROL | 4690873 |
| CONTROLS | 1046318 |
| REGISTER | 435454 |
| REGISTERS | 203532 |
| MULTIPLEXER | 112317 |
| MULTIPLEXERS | 32170 |
| MULTIPLEXOR | 8878 |
| MULTIPLEXORS | 2975 |
| MULTIPLEX | 93925 |
| MULTIPLEXES | 16534 |
| SELECT | 700467 |
| (CONTROL REGISTER SAME (MULTIPLEXER OR MULTIPLEXOR OR MULTIPLEX) SAME ((SELECT OR SELECTING) ADJ4 REGISTER)).USPT,PGPB,JPAB,EPAB,DWPI,TDBD. | 204 |

There are more results than shown above. Click here to view the entire set.

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|-----------|--|--|--|--|--|
| Search: | L8 Refine Search | | | | |
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| Set Name Query ide by side | | | Set Name result set |
|----------------------------|---|------|------------------------|
| • | SPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ | | |
| <u>L8</u> | control register same (multiplexer or multiplexor or multiplex) same ((select or selecting) adj4 register) | 204 | <u>L8</u> |
| <u>L7</u> | control register same (multiplexer or multiplexor or multiplex) | 2048 | <u>L7</u> |
| <u>L6</u> | control register same (multiplexer or multiplxor) | 1744 | <u>L6</u> |
| <u>L5</u> | (lock adj bit) same ((locking or lock or preventing or prevent or inhibit or inhibiting) adj4 (write or writing or change or changing) adj4 memory) | 13 | <u>L5</u> |
| <u>L4</u> | (lock adj bit) same ((locking or lock or preventing or prevent or inhibit or inhibiting) adj4 (write or writing or change or changing) adj4 register) | 7 | <u>L4</u> |
| <u>L3</u> | (lock adj bit) same BIOS same ((locking or lock or preventing or prevent or inhibit or inhibiting) adj4 (write or writing or change or changing) adj4 register) | 0 | <u>L3</u> |
| <u>L2</u> | (lock adj bit) same BIOS same (locking or lock or preventing or prevent or inhibit or inhibiting) same (write or writing or change or changing) same register | 3 | <u>L2</u> |
| <u>L1</u> | (lock adj bit) same (locking or lock or preventing or prevent or inhibit or inhibiting) same (write or writing or change or changing) same register | 161 | <u>L1</u> |

END OF SEARCH HISTORY

| Description Constitution of the Constitution o | 10 cmg a serie week tot | | | |
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| Search Results - | | | | |
| Term | Documents | | | |
| (27 AND 28).USPT,PGPB,JPAB,EPAB,DWPI,TDBD. | 11 | | | |
| (L27 AND L28).USPT,PGPB,JPAB,EPAB,DWPI,TDBD. | 11 | | | |
| | | | | |
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| Search History | | | | |

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| Set Name | | Hit Count | Set Name result set | | | | |
|---|---|-----------|------------------------|--|--|--|--|
| DB= $USPT$, $PGPB$, $JPAB$, $EPAB$, $DWPI$, $TDBD$; $PLUR$ = YES ; OP = ADJ | | | | | | | |
| <u>L29</u> | 127 and 128 | 11 | <u>L29</u> | | | | |
| <u>L28</u> | (lock or locking) adj4 register | 1989 | <u>L28</u> | | | | |
| <u>L27</u> | L26 and 125 | 76 | <u>L27</u> | | | | |
| <u>L26</u> | (multiplexer or multiplexor) same register | 26076 | <u>L26</u> | | | | |
| <u>L25</u> | (prevent or preventing or inhibit or inhibiting) adj4 ((write or writing or overwrite or overwriting) adj3 (register)) | 272 | <u>L25</u> | | | | |
| <u>L24</u> | BIOS same ((lock or locking) adj4 (register)) | 26 | <u>L24</u> | | | | |
| <u>L23</u> | BIOS same (lock or locking) same (register) | 85 | <u>L23</u> | | | | |
| <u>L22</u> | ((lock or locking) adj4 (register)) same (prevent or preventing or inhibit or inhibiting) same (change or changing or write or writing) | 66 | <u>L22</u> | | | | |
| <u>L21</u> | L20 same (prevent or preventing or inhibit or inhibiting) same (change or changing or write or writing) | 97 | <u>L21</u> | | | | |
| <u>L20</u> | (protect or protection or protecting) adj4 register | 1107 | <u>L20</u> | | | | |
| <u>L19</u> | register same ((hardware or software) adj2 (protected or protect or protection)) | 169 | <u>L19</u> | | | | |
| <u>L18</u> | control register same ((hardware or software) adj2 (protected or protect or protection)) | 12 | <u>L18</u> | | | | |
| <u>L17</u> | (alias or aliasing) and 116 | 3 | <u>L17</u> | | | | |
| <u>L16</u> | forwarding adj pointer | 63 | <u>L16</u> | | | | |
| <u>L15</u> | short adj2 quasi adj2 unique adj2 identifier | 2 | <u>L15</u> | | | | |
| <u>L14</u> | 13 and token | 21 | <u>L14</u> | | | | |
| <u>L13</u> | reuse count same object | 2 | <u>L13</u> | | | | |
| <u>L12</u> | 13 and 19 | 3 | <u>L12</u> | | | | |
| <u>L11</u> | L10 and 19 | 10 | <u>L11</u> | | | | |
| <u>L10</u> | object same count same handle | 263 | <u>L10</u> | | | | |
| <u>L9</u> | (deallocate or deallocation or deallocating or (de adj (allocate or allocating or allocation))) adj4 object | 203 | <u>L9</u> | | | | |
| <u>L8</u> | L6 and 12 | 0 | <u>L8</u> | | | | |
| <u>L7</u> | L6 and 12 and 11 | 0 | <u>L7</u> | | | | |
| <u>L6</u> | token same count same handle | 71 | <u>L6</u> | | | | |
| <u>L5</u> | anonymous adj token | 3 | <u>L5</u> | | | | |
| <u>L4</u> | token and 12 and 11 | 81 | <u>L4</u> | | | | |
| <u>L3</u> | transient adj2 object | 251 | <u>L3</u> | | | | |
| <u>L2</u> | (deallocate or deallocation or deallocating or (de adj (allocate or allocating or allocation))) same object | 545 | <u>L2</u> | | | | |
| <u>L1</u> | (pointer or handle or reference) same object | 460061 | <u>L1</u> | | | | |